

U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.	Serial No.
		M-15289 US	10/724,440
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s)	
		Debanjan Mukherjee et al.	
		Filing Date	Group
		11/26/2003	2838

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>AB</i>	AA	6,501,256	12/31/2002	Jaussi et al.	 		
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>AB</i>	AQ	"A CMOS Bandgap Reference Circuit With Sub-I-V Operation," by Hironori Banba et al., IEEE Journal of Solid-State Circuits, Vol. 34, No. 5, May 1999
	AR	
	AS	

Examiner

A. Buland

Date Considered

10/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.